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☐ URGENT☐ FOR REVIEW☐ PLEASE COMMENT☐ PLEASE REPLY☐ PLEASE RECYCLE

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Xiaopin Zhang et al.
Appl. No. : 09/624,438
Filed : July 24, 2000
Title : Controlable Current Source Circuit and Phase
Locked Loop Equipped Therewith
Examiner : Linh M. Nguyen
Group Art Unit : 2816

A M E N D M E N T

Hon. Commissioner of Patents and Trademarks
Washington, DC 20231

S i r :

Responsive to the Office action dated December 19, 2002,
kindly amend the above-identified application as follows:

In the Claims:

Cancel claim 11.

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Claim ~~12~~ (amended). The current source circuit according to
claim ~~13~~, wherein said first driver stage forms a part of a

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current mirror circuit receiving a flow of a stabilized current when said first driver stage is switched on.

Claim ¹~~13~~ (amended). A controllable current source circuit, comprising:

an output;

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a supply voltage terminal and a reference potential terminal;

a first driver stage having a first controlled path containing a first transistor and a second driver stage having a second controlled path containing a second transistor, said first and second controlled paths connected in series between said voltage supply terminal and said reference potential terminal, and said second driver stage forming a part of a current mirror circuit causing a constant stabilized current to flow in said second driver stage; and

only said first driver stage switching on and off in dependence on an input signal, and said second driver stage being switched on and carrying a stabilized current.

Claim ³~~14~~ (amended). The current source circuit according to claim ¹~~13~~, wherein said current mirror circuit is coupled to a current mirror circuit connected to said first driver stage